



PATENT  
Attorney's Doc. No. 8371-129

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent No.: US 6,900,083 B2  
Inventor: Voutsas et al.  
Issued: May 31, 2005

For: METHOD OF FORMING MULTI-LAYERS FOR A THIN FILM  
TRANSISTOR

Ser. No.: 09/945,063  
Group Art Unit: 2823

Filed: August 31, 2001  
Examiner: George Fourson

TRANSMITTAL LETTER

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Enclosed for filing are the following:

- ☒ Request for Certificate of Correction
- ☒ Certificate of Correction Form PTO 1050 (in duplicate)
- ☒ Return postcard
- ☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

**Certificate**  
OCT 27 2006  
**of Correction**

Respectfully submitted,

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Hosoon Lee  
Reg. No. 56,737

MARGER JOHNSON & McCOLLOM, P.C.  
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22313-1450  
Date: October 23, 2006

Kelly Andrews



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REQUEST FOR CERTIFICATE OF CORRECTION

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Alexandria, VA 22313-1450


Upon comparison of the above-identified original Letters Patent with applicant's copy of the application therefore, the following error in the original Letters Patent has been noted:

On the cover page, the title reads "METHOD OF FORMING MULTI-LAYERS FOR A THIN FILM TRANSISTOR" please replace it with --METHOD OF FORMING MULTI-LAYERS FOR A THIN FILM TRANSISTOR (TFT) AND THE DEVICE FORMED THEREBY--

Applicant respectfully requests that a Certificate of Correction calling attention to the above error be issued pursuant to Rule 322 of the Patent Office and Title 35, United States Code, Section 254. Two copies of a Certificate of Correction form are enclosed.

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

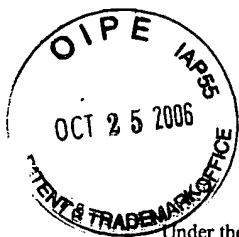
  
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## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : US 6,900,083 B2  
DATED : May 31, 2005  
INVENTOR(S) : Voutsas et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

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MAILING ADDRESS OF SENDER:  
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PATENT NO. US 6,900,083-B2

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